

/ Descriptions

1. The device is a high-precision, low-drift, and low-noise operational amplifier. It is designed for use in a wide range of applications, including signal processing, instrumentation, and control systems. The device is characterized by its high open-loop gain, high input impedance, and low output impedance. It is also capable of driving a wide range of loads, including capacitive loads. The device is available in a variety of packages, including DIP, SOIC, and TSSOP.

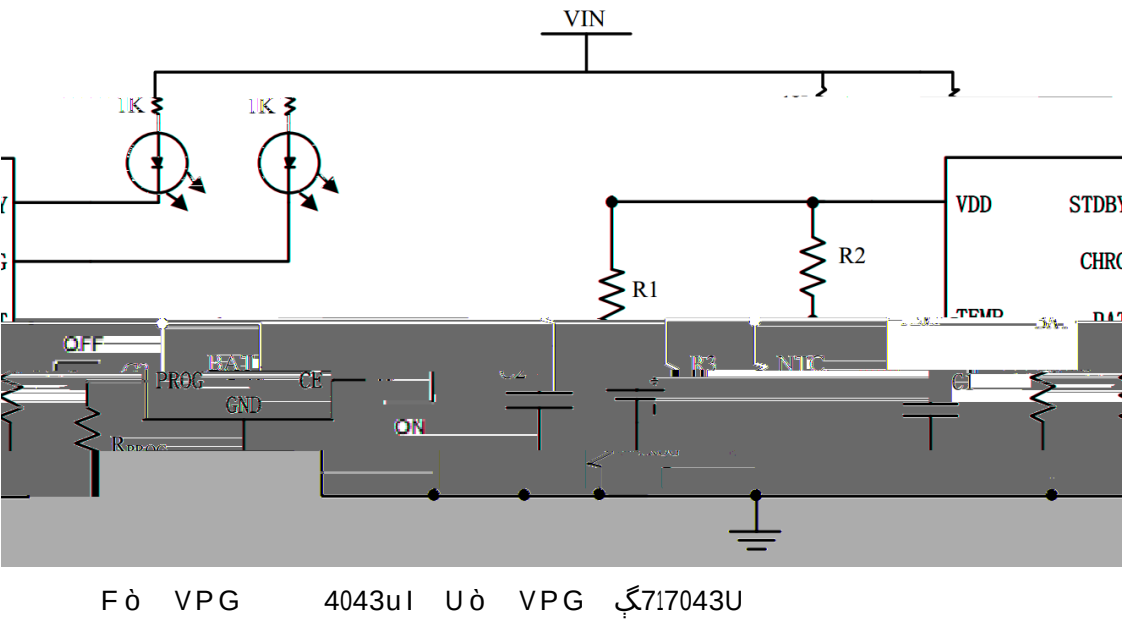
/ Features

- High precision, low drift, and low noise
- High open-loop gain
- High input impedance
- Low output impedance
- Wide bandwidth
- Low power consumption
- Wide range of operating temperatures

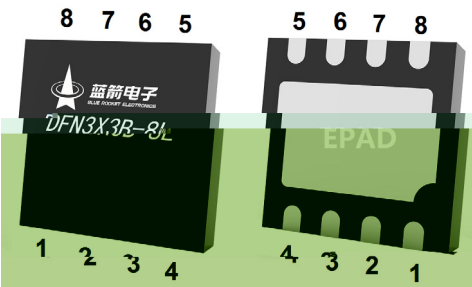
/ Applications

XVE

/ Application Circuit



/ Pinning



PIN Num.	Symbol	Function
		j
	SUR J	ꞑ
		Q
		8Y
		6
		Q ω H ʌ Q ß
		Q

/ Absolute Maximum Ratings(Ta=25)

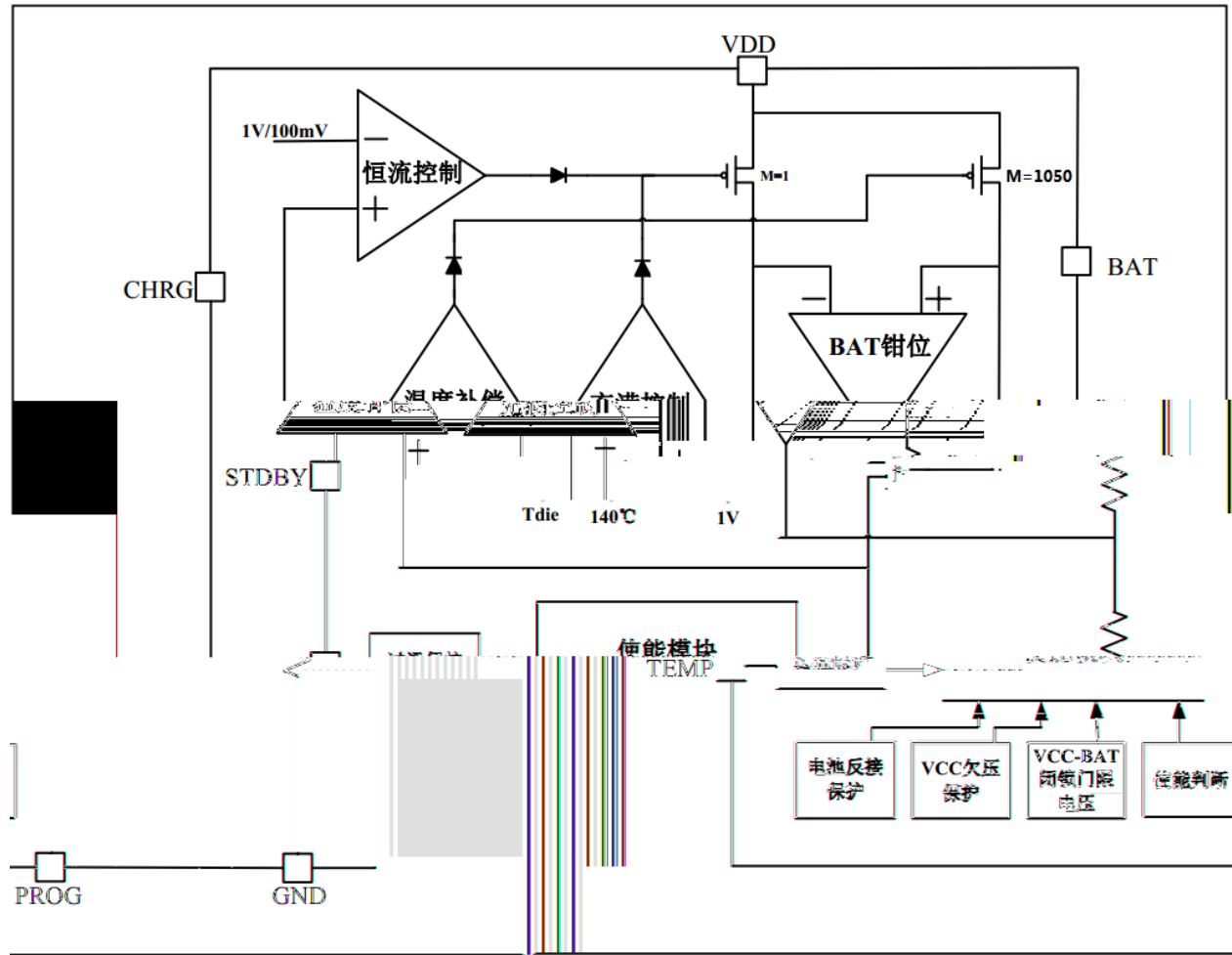
PARAMETER	SYMBOL	RATINGS	UNITS

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Power Suplly						
						μ
						μ
						μ
Charging Current Setting						

/ Electrical Characteristics(Ta=25)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Indicator Pin Status CHRG/STDBY						
Internal Temperature Compensation						
		WHPS QWF ⚡				
		WHPS QWF ⚡				

/ Principle block diagram



η ν $\mathbb{B}$ 0 τ $\mathcal{J} \varepsilon$ ζ
 i i $\cup^6$ ε 7 $\frac{1}{11}$ 6 ζ
 $\ddot{\text{I}}$ ν ε ζ $\mathcal{U}$
 ν $\star$

/ Function description

Q 6 l i i y
I 3B Q i E y

Q 3 6 3 3 i Q
ψ 6 3 Q j

I Q i j *

* i Q i j *

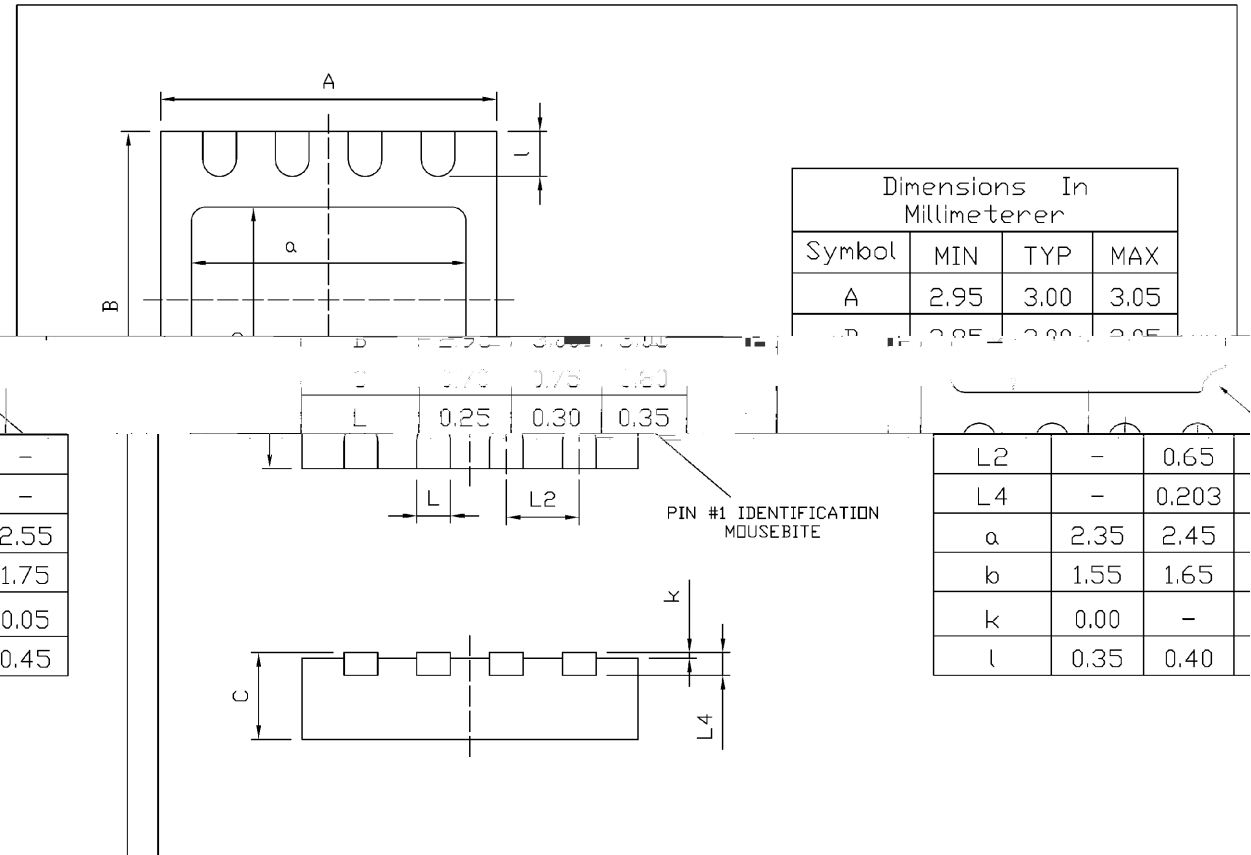
H 3 3
j I j

*		
3 H Σ +WHPS ,		
YGG j	λ	

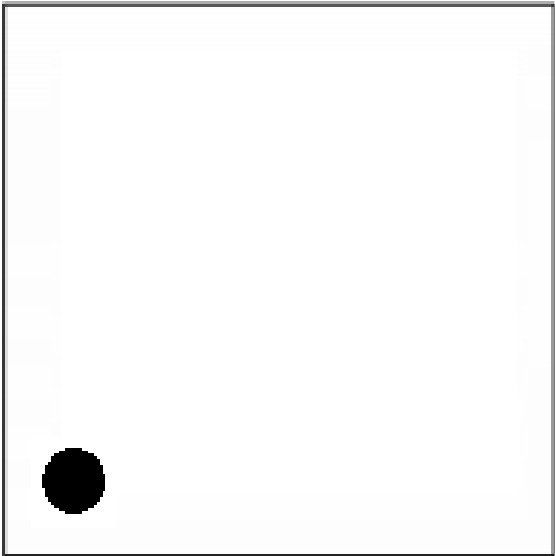
/ Package Dimensions

DFN3X3B-8L

Unit:mm



/ Marking Instructions



EU

7373

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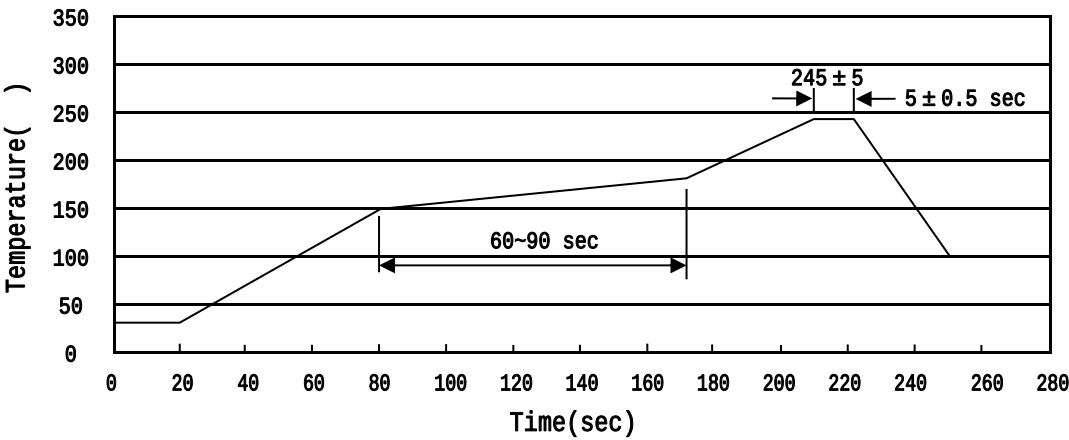
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/ Resistance to Soldering Heat Test Conditions

± ± ±

/ Packaging SPEC.

/ Notices

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